Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	373	137/386.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/07 07:36
S3	309	222/71.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:35
S4	93	250/356.1.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:35
S5	336	324/204.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:36
S6	554	700/282.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:36
S7	259	702/33.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:36
S8	432	702/45.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:36
S9	180	702/47.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:36
S10	303	702/50.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:36
S11	23	702/53.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:36
S12	307	702/127.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:37
S13	715	702/188.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 09:37
S14	582	702/189.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 10:29
S15	12	73/1.24.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S16	373	137/386.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S17	309	222/71.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S18	93	250/356.1.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S19	336	324/204.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S20	554	700/282.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S21	259	702/33.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S22	432	702/45.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S23	180	702/47.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S24	303	702/50.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24

S25	23	702/53.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S26	307	702/127.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S27	715	702/188.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S28	582	702/189.ccls.	US-PGPUB; USPAT	OR	OFF	2005/05/31 12:24
S29	3	(S15 S16 S17 S18 S19 S20 S21 S22 S23 S24 S25 S26 S27 S28) and ((first initial primary second additional secondary other another peripheral subsequent) adj (micro$controller micro$computer micro$processor microcontroller microcomputer microprocessor computer dsp (digital adj signal adj processor) plc plu (programmable adj logic adj (unit controller)) processor)) and ((sensor detector transducer) near4 (data signal information) near4 (input entry)) and ((first initial primary second additional secondary other another peripheral subsequent) adj (memory register storage)) and ((bus profibus fieldbus) near3 system)	US-PGPUB; USPAT	OR	ON	2005/05/31 12:47
S30	58	((first initial primary second additional secondary other another peripheral subsequent) adj (micro$controller micro$computer micro$processor microcontroller microcomputer microprocessor computer dsp (digital adj signal adj processor) plc plu (programmable adj logic adj (unit controller)) processor)) and ((sensor detector transducer) near4 (data signal information) near4 (input entry)) and ((first initial primary second additional secondary other another peripheral subsequent) adj (memory register storage)) and ((bus profibus fieldbus) near3 system)	US-PGPUB; USPAT	OR	ON	2005/05/31 13:21
S31	50	("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn.	US-PGPUB; USPAT	OR	OFF	2005/06/01 14:57

S32	0	("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and ((sensor transducer detector) adj independent adj (data signal information))	US-PGPUB; USPAT	OR	ON	2005/05/31 15:14
S33	35	("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and (micro$controller micro$computer micro$processor (micro adj (controller computer processor)) processor plu plc pls (programmable adj logic adj (system controller unit)) dsp (digital adj signal adj processor))	US-PGPUB; USPAT	OR	ON	2005/05/31 15:18

S34	13	("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and (micro$controller micro$computer micro$processor (micro adj (controller computer processor)) processor plu plc pls (programmable adj logic adj (system controller unit)) dsp (digital adj signal adj processor)) and $bus	US-PGPUB; USPAT	OR	ON	2005/05/31 15:20
S35	4	("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and ((plural several many multiple plurality redundant parallel) near3 (micro$controller micro$computer micro$processor (micro adj (controller computer processor)) processor plu plc pls (programmable adj logic adj (system controller unit)) dsp (digital adj signal adj processor))) and $bus	US-PGPUB; USPAT	OR	ON	2005/05/31 15:23

S36	7	("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and ((micro$controller micro$computer micro$processor (micro adj (controller computer processor)) processor plu plc pls (programmable adj logic adj (system controller unit)) dsp (digital adj signal adj processor)) with $bus)	US-PGPUB; USPAT	OR	ON	2005/05/31 15:24
S37	106109	((micro$controller micro$computer micro$processor (micro adj (controller computer processor)) processor plu plc pls (programmable adj logic adj (system controller unit)) dsp (digital adj signal adj processor)) with $bus)	US-PGPUB; USPAT	OR	ON	2005/06/01 07:37
S40	145	((micro$controller micro$computer micro$processor (micro adj (controller computer processor)) processor plu plc pls (programmable adj logic adj (system controller unit)) dsp (digital adj signal adj processor)) with (fieldbus profibus ((signal communication network) adj bus))) and (independent adj (signal data information))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 10:51
S41	4	("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and (meter metering)	US-PGPUB; USPAT	OR	ON	2005/06/01 13:02
S42	12	("5720293").URPN.	USPAT	OR	OFF	2005/06/01 13:05
S43	1	("5720293").URPN. and (meter metering)	USPAT	OR	ON	2005/06/01 13:06

S44	22	("4473797").URPN.	USPAT	OR	OFF	2005/06/01 13:46
S45	8	("4630033").URPN.	USPAT	OR	OFF	2005/06/01 13:07
S46	169	(first near4 microcontroller) and (second near4 microcontroller) and (bus near3 system)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/01 13:51
S47	1	((first near4 microcontroller) same (sensor near4 data near4 input) same (first near4 memory) same (first near4 processor)) and ((second near4 microcontroller) same (second near4 memory) same (second near4 processor)) and (bus near4 system) and (connect$3 stor$3 data instruction adapt$4 sensor enabl$3 conver$4 (sensor near3 signal) execute instruction stored convert real$time (sensor near4 measur$5 near4 signal) represent$5 measur$5 variable transfer$4 ((sensor$independent) (sensor adj independent)) processing)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:17
S48	1	((first near4 microcontroller) same (sensor near4 data near4 input) same (first near4 memory) same (first near4 processor)) and ((second near4 microcontroller) same (second near4 memory) same (second near4 processor)) and (bus near4 system)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:17
S49	1	((((first second plurality several additional many multiple secondary initial original) near4 microcontroller) same (sensor near4 data near4 input) same ((first second plurality several additional many multiple secondary initial original) near4 memory) same ((first second plurality several additional many multiple secondary initial original) near4 processor)) and (bus near4 system)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:20
S50	9	((first second plurality several additional many multiple secondary initial original) near4 microcontroller) and (sensor near4 data near4 input) and ((first second plurality several additional many multiple secondary initial original) near4 memory) and ((first second plurality several additional many multiple secondary initial original) near4 processor) and (bus near4 system)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:23
S51	9	((first second plurality several additional many multiple secondary initial original) near4 microcontroller) and (sensor near4 data near4 input) and ((first second plurality several additional many multiple secondary initial original) near4 memory) and ((first second plurality several additional many multiple secondary initial original) near4 processor) and ((bus fieldbus profibus batibus modbud eibus interbus) near4 system)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:24

S52	1	(first near4 microcontroller) and (sensor near4 data near4 input) and (first near4 memory) and (first near4 processor) and (second near4 microcontroller) and (second near4 memory) and (second near4 processor) and (bus near4 system)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/01 14:39
S53	45	microcontroller and (sensor near4 data near4 input) and memory and processor and (bus near4 system)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/01 14:28
S54	7	((plural several many multiple some additional other another couple other first second) near3 microcontroller) and (sensor near4 data near4 input) and memory and processor and (bus near4 system)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:30
S55	33	((plural several many multiple some additional other another couple other first second) near3 microcontroller) and (sensor near4 data near4 input) and memory and processor and (bus fieldbus profibus batibus modbud eibus interbus)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:31
S56	2	((plural several many multiple some additional other another couple other first second) near3 microcontroller) and (sensor near4 data near4 input) and ((plural several many multiple some additional other another couple other first second) near3 memory) and ((plural several many multiple some additional other another couple other first second) near3 processor) and (bus fieldbus profibus batibus modbud eibus interbus)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:39
S57	38	(first near4 microcontroller) and (first near4 memory) and (first near4 processor) and (second near4 microcontroller) and (second near4 memory) and (second near4 processor) and (bus fieldbus profibus batibus modbud eibus interbus)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/01 14:41
S58	3039	(first near4 (microcontroller controller microprocessor microcomputer computer)) and (first near4 (memory cache register storage)) and (first near4 processor) and ((second other additional secondary) near4 (microcontroller controller microprocessor microcomputer computer)) and ((second other additional secondary) near4 (memory cache register storage)) and ((second other additional secondary) near4 processor) and (bus fieldbus profibus batibus modbud eibus interbus)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:44

S59	586	((first near4 (microcontroller controller microprocessor microcomputer computer)) same (first near4 (memory cache register storage)) same (first near4 processor)) and (((second other additional secondary) near4 (microcontroller controller microprocessor microcomputer computer)) same ((second other additional secondary) near4 (memory cache register storage)) same ((second other additional secondary) near4 processor)) and (bus fieldbus profibus batibus modbud eibus interbus)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:47
S60	1	((first near4 (microcontroller controller microprocessor microcomputer computer)) same (first near4 (memory cache register storage)) same (first near4 processor)) and (((second other additional secondary) near4 (microcontroller controller microprocessor microcomputer computer)) same ((second other additional secondary) near4 (memory cache register storage)) same ((second other additional secondary) near4 processor)) and (bus fieldbus profibus batibus modbud eibus interbus) and ((first near4 (memory cache register storage)) same ((sensor detector transducer) with (related associated correlated) with (data information instruction)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:52
S61	4	((first near4 (microcontroller controller microprocessor microcomputer computer)) same (first near4 (memory cache register storage)) same (first near4 processor)) and (((second other additional secondary) near4 (microcontroller controller microprocessor microcomputer computer)) same ((second other additional secondary) near4 (memory cache register storage)) same ((second other additional secondary) near4 processor)) and (bus fieldbus profibus batibus modbud eibus interbus) and ((first near4 (memory cache register storage)) same ((sensor detector transducer) with (data information instruction)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:52

S62		2	((first near4 (microcontroller controller microprocessor microcomputer computer)) same (first near4 (memory cache register storage)) same (first near4 processor)) and (((second other additional secondary) near4 (microcontroller controller microprocessor microcomputer computer)) same ((second other additional secondary) near4 (memory cache register storage)) same ((second other additional secondary) near4 processor)) and (bus fieldbus profibus batibus modbud eibus interbus) and ((sensor detector transducer) with (signal data information instruction)) and ((transfer transferring communicate communicating communication transferred communicated send sending transmit transmitting transmitted transmission) with ((sensor detector transducer) with (signal data information instruction)) with ((second other additional secondary) near4 (microcontroller controller microprocessor microcomputer computer)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/01 14:55

| S63 | 532513 | ("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and (first near3 (microcontroller controller microprocessor microcomputer computer)) (first near3 (memory cache register storage)) (first near3 processor) ((second other additional secondary) near3 (microcontroller controller microprocessor microcomputer computer)) ((second other additional secondary) near3 (memory cache register storage)) ((second other additional secondary) near3 processor) (bus fieldbus profibus batibus modbud eibus interbus) ((sensor detector transducer) with (signal data information instruction)) and ((transfer transferring communicate communicating communication transferred communicated send sending transmit transmitting transmitted transmission) with ((sensor detector transducer) with (signal data information instruction)) with ((second other additional secondary) near3 (microcontroller controller microprocessor microcomputer computer))) | US-PGPUB; USPAT | OR | OFF | 2005/06/01 15:00 |

| S64 | 0 | ("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and (first near3 (microcontroller controller microprocessor microcomputer computer)) and (first near3 (memory cache register storage)) and (first near3 processor) and ((second other additional secondary) near3 (microcontroller controller microprocessor microcomputer computer)) and ((second other additional secondary) near3 (memory cache register storage)) and ((second other additional secondary) near3 processor) and (bus fieldbus profibus batibus modbud eibus interbus) and ((sensor detector transducer) with (signal data information instruction)) and ((transfer transferring communicate communicating communication transferred communicated send sending transmit transmitting transmitted transmission) with ((sensor detector transducer) with (signal data information instruction)) with ((second other additional secondary) near3 (microcontroller controller microprocessor microcomputer computer))) | US-PGPUB; USPAT | OR | OFF | 2005/06/01 15:01 |

| S65 | 2 | ("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and (microcontroller controller microprocessor microcomputer computer) and (memory cache register storage) and processor and (bus fieldbus profibus batibus modbud eibus interbus) and ((sensor detector transducer) with (signal data information instruction)) and ((transfer transferring communicate communicating communication transferred communicated send sending transmit transmitting transmitted transmission) with ((sensor detector transducer) with (signal data information instruction)) with (microcontroller controller microprocessor microcomputer computer)) | US-PGPUB; USPAT | OR | OFF | 2005/06/01 15:04 |
| S66 | 9 | ("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and (microcontroller controller microprocessor microcomputer computer) and (memory cache register storage) and processor and (bus fieldbus profibus batibus modbud eibus interbus) and ((sensor detector transducer) with (signal data information instruction)) | US-PGPUB; USPAT | OR | OFF | 2005/06/01 15:05 |

S67	2	("5943517" "4757454" "4792910" "4851896" "4894730" "5214713" "5317394" "5559610" "6246968" "4359622" "4482978" "4516213" "4528595" "4858615" "4903212" "4979388" "5008843" "5259063" "5305159" "5606707" "5720293" "5784328" "5818356" "5836982" "5841724" "5859712" "5909287" "6081821" "6173388" "4276600" "4312315" "4369501" "4429266" "4466088" "4473797" "4567462" "4616326" "4630033" "4805227" "4837711" "4845620" "4862365" "4866259" "4878072" "4878444" "4878754" "4899228" "4904882" "4920500" "4940052").pn. and ((plural multiple several many plurality series parallel) near3 (microcontroller controller microprocessor microcomputer computer)) and (memory cache register storage) and processor and (bus fieldbus profibus batibus modbud eibus interbus) and ((sensor detector transducer) with (signal data information instruction))	US-PGPUB; USPAT	OR	OFF	2005/06/01 15:06
S68	12	73/1.24.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/01 15:07
S69	76	diewald.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/06/02 09:10
S70	16	diewald-horst.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/06/02 09:19
S71	16	diewald-horst.in. and (memory stores data instruction configured adapted sensor enable conversion signal provided representing variable measured processor connected execute convert real$time measuring represent measurable variable transfer bus system second microcontroller sensor$independent transferred first)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/06/02 09:39
S72	15	diewald-horst.in. and (memory data instruction sensor signal measured processor real$time measuring represent measurable variable bus system second microcontroller sensor$independent transferred first)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/06/02 09:24

S73		15	diewald-horst.in. and (memory sensor signal processor real$time bus system second microcontroller sensor$independent transferred first)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/06/02 09:30
S74		1	de-10129300-$.did.	DERWENT	OR	OFF	2005/06/02 09:34
S75		1	de-4311614-$.did.	DERWENT	OR	OFF	2005/06/02 09:31
S76		1	de-3142468-$.did.	DERWENT	OR	OFF	2005/06/02 09:31
S77		1	de-10002275-$.did.	DERWENT	OR	OFF	2005/06/02 09:31
S78		1	de-29521924-$.did.	DERWENT	OR	OFF	2005/06/02 09:32
S79		1	de-3307459-$.did.	DERWENT	OR	OFF	2005/06/02 09:32
S80		1	de-3612121-$.did.	DERWENT	OR	OFF	2005/06/02 09:32
S81		1	wo-200036380-$.did.	DERWENT	OR	OFF	2005/06/02 09:33
S82		8	(S74 S75 S76 S77 S78 S79 S80 S81)	DERWENT	OR	OFF	2005/06/02 10:16
S83		0	(S74 S75 S76 S77 S78 S79 S80 S81) and ((sensor adj independent) sensor$independent)	DERWENT	OR	ON	2005/06/02 09:36
S84		0	(S74 S75 S76 S77 S78 S79 S80 S81) and (((sensor adj independent) sensor$independent) near3 (data information instruction command signal))	DERWENT	OR	ON	2005/06/02 09:37
S85		0	(S74 S75 S76 S77 S78 S79 S80 S81) and (((sensor adj independent) sensor$independent) with (data information instruction command signal))	DERWENT	OR	ON	2005/06/02 09:38
S86		0	(S74 S75 S76 S77 S78 S79 S80 S81) and (((sensor adj independent) sensor$independent) same (data information instruction command signal))	DERWENT	OR	ON	2005/06/02 09:38
S87		0	(S74 S75 S76 S77 S78 S79 S80 S81) and ((sensor adj independent) sensor$independent) and (data information instruction command signal)	DERWENT	OR	ON	2005/06/02 10:17
S88		1	diewald-horst.in. and (((sensor adj independent) sensor$independent) near3 (data information instruction command signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/06/02 09:39
S89		9	(S70 S74 S75 S76 S77 S78 S79 S80 S81) and (consumption usage tariff rate cost price pricing valuation)	US-PGPUB; USPAT; DERWENT	OR	ON	2005/06/02 10:09
S90		1	"6053030".pn.	USPAT	OR	OFF	2005/06/02 10:16
S91		9	("4570217" | "4835521" | "4869874" | "4900252" | "5289372" | "5469369" | "5586050" | "5632957" | "5905436").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/02 10:16
S92		4	("6053030").URPN.	USPAT	OR	OFF	2005/06/02 10:17

S93	0	(S90 S91) and ((sensor adj independent) sensor$independent) and (data information instruction command signal)	USPAT	OR	ON	2005/06/02 10:20
S94	4	(S90 S91) and (microcontroller controller microprocessor processor microcomputer computer plu plc pls (programmable adj logic adj (system controller unit))) and (bus fieldbus profibus scbus scxbus modbus eibus)	USPAT	OR	ON	2005/06/02 10:24
S95	3	(S90 S91) and ((plural many several plurality multiple) near3 (microcontroller controller microprocessor processor microcomputer computer plu plc pls (programmable adj logic adj (system controller unit)))) and (bus fieldbus profibus scbus scxbus modbus eibus)	USPAT	OR	ON	2005/06/02 10:44
S96	0	((first near3 microcontroller) same input same sensor same data same (first near3 memory) same (first near3 processor)) and ((second near3 microcontroller) same (second near3 memory) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same connect (first near3 microcontroller) same (second near3 microcontroller))	USPAT	OR	ON	2005/06/02 11:15
S97	1	((first near3 microcontroller) same input same sensor same data same (first near3 memory) same (first near3 processor)) and ((second near3 microcontroller) same (second near3 memory) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same connect (first near3 microcontroller) same (second near3 microcontroller))	US-PGPUB	OR	ON	2005/06/02 10:49
S98	0	((first near3 microcontroller) same input same sensor same data same (first near3 memory) same (first near3 processor)) and ((second near3 microcontroller) same (second near3 memory) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same connect (first near3 microcontroller) same (second near3 microcontroller))	USOCR	OR	ON	2005/06/02 10:49
S99	0	((first near3 microcontroller) same input same sensor same data same (first near3 memory) same (first near3 processor)) and ((second near3 microcontroller) same (second near3 memory) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same connect (first near3 microcontroller) same (second near3 microcontroller))	EPO; JPO; DERWENT	OR	ON	2005/06/02 10:50

S100	0	((first near3 microcontroller) same input same sensor same data same (first near3 memory) same (first near3 processor)) and ((second near3 microcontroller) same (second near3 memory) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same connect (first near3 microcontroller) same (second near3 microcontroller))	IBM_TDB	OR	ON	2005/06/02 10:50
S101	0	((first near3 microcontroller) same (first near3 memory) same (first near3 processor)) and ((second near3 microcontroller) same (second near3 memory) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same connect (first near3 microcontroller) same (second near3 microcontroller))	IBM_TDB	OR	ON	2005/06/02 10:50
S102	0	((first near3 microcontroller) same (first near3 memory) same (first near3 processor)) and ((second near3 microcontroller) same (second near3 memory) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same connect (first near3 microcontroller) same (second near3 microcontroller))	EPO; JPO; DERWENT	OR	ON	2005/06/02 10:50
S103	5	((first near3 microcontroller) same (first near3 memory) same (first near3 processor)) and ((second near3 microcontroller) same (second near3 memory) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same connect (first near3 microcontroller) same (second near3 microcontroller))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/02 10:52
S104	0	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and ((bus fieldbus scbus scxbus eibus modbus interbus profibus) with connect with (first near3 microcontroller) with (second near3 microcontroller))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/02 10:55
S105	16	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and (bus fieldbus scbus scxbus eibus modbus interbus profibus) and ((first near3 microcontroller) with (second near3 microcontroller))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/02 11:06

S106	6	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and ((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) and ((first near3 microcontroller) with (second near3 microcontroller))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/02 11:00
S107	1	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and ((input inputting inputted entry enter entering) near4 (transducer detector sensor) near3 (data information instruction)) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) with ((first near3 microcontroller) with (connect$3 attach$5 join$3 communicat$3) with (second near3 microcontroller)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/02 11:04
S108	1	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and ((input inputting inputted entry enter entering) near4 (transducer detector sensor) near3 (data information instruction)) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) with ((first near3 microcontroller) with (second near3 microcontroller)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/02 11:04
S109	1	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and ((input inputting inputted entry enter entering) near4 (transducer detector sensor) near3 (data information instruction)) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and ((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) and ((first near3 microcontroller) with (second near3 microcontroller))	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/02 11:05

S110	0	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and ((input inputting inputted entry enter entering) near4 (transducer detector sensor) near3 (data information instruction)) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and ((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) and ((first near3 microcontroller) with (second near3 microcontroller))	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/06/02 11:05
S111	0	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and (bus fieldbus scbus scxbus eibus modbus interbus profibus) and ((first near3 microcontroller) with (second near3 microcontroller))	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/06/02 11:07
S112	0	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and (bus fieldbus scbus scxbus eibus modbus interbus profibus)	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/06/02 11:07
S113	29	(first near3 microcontroller) and (first near3 memory) and (first near3 processor) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and (bus fieldbus scbus scxbus eibus modbus interbus profibus)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/02 11:12
S114	0	("4432064" "4516213").pn. and (first near3 microcontroller) and (first near3 memory) and (first near3 processor) and (second near3 microcontroller) and (second near3 memory) and (second near3 processor) and (bus fieldbus scbus scxbus eibus modbus interbus profibus)	USPAT	OR	ON	2005/06/02 11:13
S115	2	("4432064" "4516213").pn. and ((first near3 microcontroller) (first near3 memory) (first near3 processor) (second near3 microcontroller) (second near3 memory) (second near3 processor) (bus fieldbus scbus scxbus eibus modbus interbus profibus))	USPAT	OR	ON	2005/06/02 11:13

| S116 | | 0 | ((first near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) same input same ((transducer detector sensor) near3 (data information signal instruction)) same (first near3 (memory register storage)) same (first near3 processor)) and (((second near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) same (second near3 (memory register storage))) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same ((first near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) same (second near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))))) | USPAT | OR | ON | 2005/06/02 11:24 |
| S117 | | 0 | ((first near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) same (input inputting inputted entry enter entering) same ((transducer detector sensor) near3 (data information signal instruction)) same (first near3 (memory register storage)) same (first near3 processor)) and (((second near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) same (second near3 (memory register storage))) same (second near3 processor)) and (((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) same ((first near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) same (second near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))))) | USPAT | OR | ON | 2005/06/02 11:25 |

| S118 | 147 | (first near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) and ((input inputting inputted entry enter entering) same ((transducer detector sensor) near3 (data information signal instruction))) and (first near3 (memory register storage)) and (first near3 processor) and (second near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) and (second near3 (memory register storage)) and (second near3 processor) and ((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) and (first near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) and (second near3 (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) | USPAT | OR | ON | 2005/06/02 11:30 |
| S119 | 13 | (first adj (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) and ((input inputting inputted entry enter entering) same ((transducer detector sensor) near3 (data information signal instruction))) and (first adj (memory register storage)) and (first adj processor) and (second adj (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) and (second adj (memory register storage)) and (second adj processor) and ((bus fieldbus scbus scxbus eibus modbus interbus profibus) near3 system) and (first adj (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) and (second adj (microcontroller microprocessor microcomputer computer processor plu pls plc (programmable adj logic adj (unit controller system)))) | USPAT | OR | ON | 2005/06/02 11:37 |

| S120 | 22 | (first adj (microcontroller microcomputer microprocessor processor computer controller (programmable adj logic (unit controller system device apparatus)))) and ((second additional secondary) adj (microcontroller microcomputer microprocessor processor computer controller (programmable adj logic (unit controller system device apparatus)))) and (first adj memory) and ((second additional secondary) adj memory) and (fieldbus profibus interbus eibus modbus scbus scxbus) | US-PGPUB; USPAT | OR | OFF | 2005/06/02 15:14 |
| S121 | 6 | (first adj (microcontroller microcomputer microprocessor processor computer controller (programmable adj logic (unit controller system device apparatus)))) and ((second additional secondary) adj (microcontroller microcomputer microprocessor processor computer controller (programmable adj logic (unit controller system device apparatus)))) and (first adj memory) and ((second additional secondary) adj memory) and (fieldbus profibus interbus eibus modbus scbus scxbus) and ((sensor transducer detector) adj (data information signal output)) | US-PGPUB; USPAT | OR | OFF | 2005/06/02 15:16 |